

GOVERNMENT ENGINEERING COLLEGE, THRISSUR
NOTICE INVITING TENDER

D2/6169/23/GECTCR
11.08.2023
E Tender No. : D2/29/23-24

Superscription : Purchase of DSP Trainer kit for
Electronics Department

Bidding fee : Rs. 708/-(600 +18%GST)
EMD required : Rs. 3000/-

Address of the Officer to whom hardcopy is to be sent :
THE PRINCIPAL,
GOVERNMENT ENGINEERING COLLEGE,
RAMAVARMAPURAM, THRISSUR, 680009

ITEM SPECIFICATION

Sl.no	Items with specifications	Quantity
	<p>TMS320C6748 based DSP Trainer kit with speaker and mic for Codec experiments (Detailed specification attached)</p> <p><u>Detailed Specifications</u></p> <p>C6748 low-power digital signal processor based on C674x DSP core. It consumes significantly lower power than other members of the TMS320C6000™ platform of DSPs.</p> <p>On-Chip Features:</p> <ul style="list-style-type: none">• TMS320C674x Fixed/Floating-Point VLIW DSP Core• 64 General-Purpose Registers (32 Bit)• Six ALU (32-/40-Bit) Functional Units• Two Multiply Functional Units• Instruction Packing Reduces Code Size• All Instructions Conditional• Hardware Support for Modulo Loop Operation• Real time clock <p>On-Chip Memory:</p> <p>* Two Level Cache Memory Architecture</p> <ul style="list-style-type: none">• 32K-Byte L1P Program RAM/Cache• 32K-Byte L1D Data RAM/Cache	

- 256K-Byte L2 Unified Mapped RAM/Cache

- * 128K-Byte RAM Shared Memory

On-Board Features:

- 2 Meg x 16 Bit x 4 Banks Synchronous DRAM for External Program/Data

- **Codec:**

- Stereo Audio ADC & DAC
- 16/20/24/32-Bit Data Format
- Supports Rates From 8 kHz to 96 kHz
- Programmable Input /Output Analog Gains
- Audio Serial Data Bus Supports I2S, Left/Right-Justified, DSP, and TDM Modes

- **On Board ADC/DAC**

- **ADC:**

- No of ADC input Channels : 2
- Resolution bit: 12
- Sampling rate(max)1Msps
- Analog input range(max): + 5V
- High speed SPI serial interface

- **DAC:**

- No of DAC outputChannels: 2
- Resolutionbit: 12
- Settling time micro sec (max)8.5
- Analog output range (max): + 5V
- High speed SPI serial interface

- **Extra Features:**

- Provision to connect external JTAG Emulator
- 8 user definable LEDs
- 8 position DIP switch, user definable
- Two 3.5 mm. audio jacks for audio IN and OUT
- Expansion connector for GPIO / Video Add on card

- **JTAG Emulator(XDS100V1):**

Emulation Connect/Disconnect, Read/Write memory, Read registers, Load program, Run, Halt, Step, Software and Hardware Breakpoint support, Real-Time Mode. Support for USB Full Speed (12 Mbits/s)

List of Deliverables should include:

Hardware

- 6748 Trainer Kit
- USB Cable
- Power Supply +5V Adaptor
- Software CD
- Program & Manual CD

USER MANUAL WITH LIST OF EXPERIMENTS

- Sampling Theorem
- Impulse Response
- Linear Convolution & Linear Convolution using DFT & IDFT
- Circular Convolution & Circular Convolution using DFT & IDFT
- Auto Correlation
- Cross Correlation
- Solving Difference Equations
- N point DFT and FFT
- Design FIR Filter
- Design IIR Filter
- Noise Removal
- Waveform Generation
- CODEC Loop back, Voice Storing & Retrieval etc.,

Installation, demonstration and and training for the faculty and staff for one day

General conditions

1. The unit price, all other charges such as delivery, transporting, packing, shipping, loading and unloading charges etc, and GST must be shown separately and should be furnished unambiguously.
2. Payment will be made only after the successful supply, installation and testing.
3. F.O.R: Govt. Engineering College, Thrissur.
4. Agreement: Preliminary Agreement in Rs.220/- Kerala Stamp Paper.
5. Date of opening of tender: In case the proposed date declared as holiday, the tender will be opened on the next working day.
6. After E-tendering the hard copy of all documents should be submitted before the date of opening of the tender to the Principal, Government Engineering College, Thrissur.
7. Items to be supplied at Civil Engineering of Govt. Engineering College, Thrissur.
8. The items should have a minimum guarantee period of one year from the date of installation and successful performance.
9. Installation, successful demonstration and training required.
10. Delivery Period: Immediately after the date of receipt of supply order within two months
11. 5% security deposit along with agreement should be furnished within a month/fortnight from the date of receipt of supply order.
12. Only GST Registered firms should participate in the tender.

13. Bidder shall be responsible for **installation** / demonstration as applicable and for after sales service during the **warranty** and thereafter.
14. Installation and demonstration to **be arranged** by the supplier free of cost and the same is to be done within 15 days of the arrival of the equipment at site.

NB: The Tender procedure will be made **as per** Rules mentioned in the Revised Store Purchase Manual. The bidders should **participate** this tender through E-Tendering System. Tender cost and EMD should **be submitted** only through online. For more details Contact Ph.0487 2334144.

sd/-

Principal

Approval Valid

Digitally Approved By

Dr. Satish K P

Date: 11.08.2023

Reason: Approved

Forwarded / By Order

Accounts Officer

For

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Kerala Tenders		eTendering System Government of Kerala	
Tender Details		Date : 21-Sep-2023 03:15 PM	
Print			
Basic Details			
Organisation Chain	Directorate of Technical Education Govt. Engineering College Thrissur		
Tender Reference Number	D2/6169/2023/GECTCR		
Tender ID	2023_DTE_605393_1	Withdrawal Allowed	Yes
Tender Type	Open Tender	Form of contract	Item Wise
Tender Category	Goods	No. of Covers	2
General Technical Evaluation Allowed	No	ItemWise Technical Evaluation Allowed	No
Payment Mode	Online	Is Multi Currency Allowed For BOQ	No
Is Multi Currency Allowed For Fee	No	Allow Two Stage Bidding	No
Payment Instruments		Cover Details, No. Of Covers - 2	
Online Bankers	S.No	Bank Name	
	1	SBI MOPS	
	Cover No	Cover	Document Type
	1	Fee/PreQual /Technical	.pdf
			.pdf
			.pdf
			.pdf
			.pdf
			.pdf
	2	Finance	.xls
Tender Fee Details, [Total Fee in ₹ * - 708]		EMD Fee Details	
Tender Fee in ₹	708	EMD Amount in ₹	3,000
Fee Payable To	Nil	EMD through BG/ST or EMD Exemption Allowed	Yes
Fee Payable At	Nil		

Tender Fee Exemption Allowed	Yes	EMD Fee Type	percentage	EMD Percentage	1.0%
		EMD Payable To	Nil	EMD Payable At	Nil

[Click to view modification history](#)

Work / Item(s)					
Title	Purchase of DSP Trainer Kit				
Work Description	Purchase of DSP Trainer Kit for Electronics Department				
Pre Qualification Details	Please refer Tender documents.				
Independent External Monitor/Remarks	NA				
Show Tender Value in Public Domain	Yes				
Tender Value in ₹	3,00,000	Product Category	Equipments	Sub category	NA
Contract Type	Tender	Bid Validity(Days)	180	Period Of Work(Days)	60
Location	GOVERNMENT ENGINEERING COLLEGE, THRISSUR	Pincode	680009	Pre Bid Meeting Place	NA
Pre Bid Meeting Address	NA	Pre Bid Meeting Date	NA	Bid Opening Place	GOVERNMENT ENGINEERING COLLEGE, THRISSUR
Should Allow NDA Tender	No	Allow Preferential Bidder	No		

Critical Dates

Publish Date	21-Sep-2023 05:00 PM	Bid Opening Date	28-Oct-2023 11:00 AM
Document Download / Sale Start Date	21-Sep-2023 05:00 PM	Document Download / Sale End Date	26-Oct-2023 02:00 PM
Clarification Start Date	NA	Clarification End Date	NA
Bid Submission Start Date	21-Sep-2023 06:00 PM	Bid Submission End Date	26-Oct-2023 02:00 PM

Tender Documents

NIT Document	S.No	Document Name	Description	Document Size (in KB)	
	1	Tendernotice_1.pdf	TENDER NOTICE	93.06	
	2	Tendernotice_2.pdf	TENDER FORM	2405.76	
	3	Tendernotice_3.pdf	AGREEMENT	81.79	
	4	Tendernotice_4.pdf	SPECIFICATION	163.35	
Work Item Documents	S.No	Document Type	Document Name	Description	Document Size (in KB)
	1	Tender Documents	BOQ.xls	BOQ	292.00

Bid Openers List

S.No	Bid Opener Login Id	Bid Opener Name	Certificate Name
1.	kgjestin@gmail.com	JESTIN K G	JESTIN K G
2.	ursdhanya@gmail.com	DHANYA V S	DHANYA V S
3.	leethajoseph@gmail.com	LEETHA JOSEPH	LEETHA JOSEPH

Tender Properties

Auto Tendering Process allowed	No	Show Technical bid status	Yes
Show Finance bid status	Yes	Show Bids Details	Yes
BoQ Comparative Chart model	Normal	BoQ Compative chart decimal places	2
BoQ Comparative Chart Rank Type	L	Form Based BoQ	No
Show Bid Details in Public Domain stage	Technical Bid Opening		

Tender Inviting Authority

Name	Principal
Address	GOVERNMENT ENGINEERING COLLEGE, THRISSUR

Tender Creator Details

Created By	LEETHA JOSEPH
Designation	Junior Superintendent
Created Date	21-Sep-2023 02:58 PM