APPLICATION FORM

Faculty and Staff Development Training Programme

Fundamentals of FPGA Based System Design

16th to 21st December 2019

1. Name:

2. Designation:

3. Institution / Department:

4. Age:

5. Gender: Male/ Female

6. Qualifications:

7. Address for communication:

8. Mobile No:

9. Email:

10. Accommodation required or not: Yes/No

11. Food preference: Veg / Non veg

DECLARATION

The above mentioned information is true to the best of my knowledge and belief. I agree to abide by the rules and regulations governing the course. I also undertake that I will attend the course for the entire duration, if selected.

Place:

Date: Signature of the Applicant

SPONSORSHIP

Certified that Dr/Mr/Mrs ……………………………… …...…………………….. is a permanent employee of this institution. He/she will be permitted to attend the course, if selected.

Place:

Date: (Office Seal)   Signature of Head
                                 of the Institution

WHO SHOULD ATTEND?

Regular faculty / technical staff members of Electronics / Electrical / Computer Science Engineering and allied branches of AICTE approved Govt./Aided/ Self financing Engineering Colleges, Polytechnics and Technical High Schools in Kerala state are eligible to apply for this programme. The number of participants is limited to 30. Admission to the programme is on first come first serve basis.

REGISTRATION FEE

No Registration fee will be charged from the participants of Govt./Aided Institutions. Registration fee Rs.1000/- for all other participants.

TRAVELLING ALLOWANCE

Participants of Govt./Aided Institutions are eligible for travelling allowance as per FSDTC norms. Accommodation can be arranged for the participants on prior request.

HOW TO APPLY

Application for admission to the course should be submitted online through the link :

http://admissions.dtekerala.gov.in/tpms/homeOA

COORDINATOR
Ms. Alphonsa M. P
Mob. No. 94006 60759

CO-COORDINATOR
Ms. Jainy Paul
Mob. No. 94957 08270

HEAD OF THE DEPARTMENT
Dr. Anilkumar C.D
Mob. No. 94472 25933

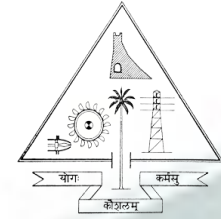## IMPORTANT DATES
Last date of receipt of application: 05-12-2019

Scan to Apply

We follow Green Protocol
Please cooperate with us.

The Faculty and Staff Development Training Centre
Govt. Engineering College Thrissur

Faculty and Staff Development Training Programme
On
Fundamentals of FPGA Based System Design
16th to 21st December 2019

Organized by
Department of Electronics & Communication Engineering
Govt. Engineering College Thrissur
Thrissur - 680009, Kerala, India

Sponsored by
Director of Technical Education
Government of Kerala

ABOUT FSDTC

The objective of Faculty and Staff Development Training Centre, Govt. Engineering College, Thrissur is to provide a suitable platform for personal, academic and professional development of the faculty members and the supporting staff of Engineering Colleges in Kerala. The centre strives to improve and strengthen the process of teaching, learning and retention by inculcating a new culture in the system. It manages training programmes on the basis of detailed training need analysis (TNA) among the faculty/staff of engineering colleges in the state. In addition to scientific/technical subject areas, courses on interpersonal relationship, communication skill, E- governance, stress management etc. that are of vital importance in the present time, is organized to enhance the efficiency of academic system.

ABOUT THE COLLEGE

Government Engineering College Thrissur (GEC Thrissur), an ISO 9001: 2008 certified institution, is one of the most prestigious and reputed technical education institution in south India. The College was established in the year 1957 with under graduate programmes in Civil, Mechanical and Electrical engineering branches. The foundation stone of the college main building was laid down by Pandit Jawaharlal Nehru. The college campus spread over an area of more than 75 acres is located at Ramavarmapuram, about 5 km from Thrissur city. Today, the institution has made substantial progress by way of offering eight undergraduate, twelve post-graduate and doctoral programmes in different disciplines. All the UG and majority of the PG programmes are accredited by National Accreditation Board. The start-up initiative of the Technology Business Incubator functioning in the college is growing at a great pace with more than ten start-up organization at present.

ABOUT ELECTRONICS AND COMMUNICATION ENGINEERING DEPARTMENT

The Department of Electronics & Communication Engineering started in the year 1983 for B.Tech with a current intake of 63 students. In 2010, an M.Tech program in Communication Engineering & Signal Processing began with an intake of 18 students. Spacious laboratories, class rooms, seminar hall, waiting rooms for boys & girls, library and plenty of other amenities are the main attributes of our department. In 2016, the department became a Research Centre of APJAKTU.

Hard working students, faculty and technical supporting staff are the distinctive characteristics of the Department of Electronics & Communication Engineering. Research guidance is offered by faculty of the department in the field of Digital Signal processing & Statistical signal processing. The department has been accredited by NBA for three years in 2017, and is a regularly ISO audited department.

DEPARTMENT'S VISION

To become a nationally acclaimed Department of higher learning and research that will serve as a source of knowledge and expertise in Electronics & Communication Engineering.

DEPARTMENT'S MISSION

1. To provide quality education in the area of Electronics & Communication Engineering, for producing innovative and ethically driven professionals adept at dealing with a globally competitive environment, for the welfare of the nation.

2. To inculcate inquisitiveness in young graduates thereby persuading them to undertake research in emerging areas of Electronics & Communication Engineering.

ABOUT THE COURSE

Field Programmable Gate Arrays (FPGAs) are semiconductor devices that are based around a matrix of configurable logic blocks (CLBs) connected via programmable interconnects. FPGAs can be reprogrammed to desired application or functionality requirements after manufacturing. This feature distinguishes FPGAs from Application Specific Integrated Circuits (ASICs), which are custom manufactured for specific design tasks. Although one-time programmable (OTP) FPGAs are available, the dominant types are SRAM based which can be reprogrammed as the design evolves.

- Introduction to HDL Basic and Coding examples.
- Fundamentals of Digital Sequential circuits and FSM design.
- Introduction to Xilinx Vivado Tool and VHDL simulation.
- Lab on HDL coding and simulation using Xilinx, Vivado Tool.
- Briefing on FPGA Fundamentals and types.
- Briefing on Project design and Flow using FPGA.
- Lab on HDL code synthesis and implementation of FPGA Board.
- IP core based design on FPGA.
- Lab on IP core based design of FPGA.
- Project Discussions and Designing of Group Project.
- Final implementation of all learned concepts into a Project Design.
- Demo and Project Presentation.

RESOURCE PERSONS

Eminent professors and scientists from reputed engineering institutions and research centres are handling sessions in the course.